

ABSTRACT OF THE DISCLOSURE

5 An impedance blocking filter circuit is provided for
use in telecommunication systems for interconnecting
between incoming telephone lines and customer's terminal
equipment so as to unconditionally block impedances above
20 KHz due to the customer's terminal equipment from an
ADSL network unit and/or home networking interface unit.
The filter circuit includes first, second, and third
inductors connected in series between a first input
10 terminal and a first common point. A first resistor has
its one end connected also to the first common point and
its other end connected to a first output terminal.
Fourth, fifth and sixth inductors are connected in series
between a second input terminal and a second common
15 point. A second resistor has its one end also connected
to the second common point and its other end connected to
a second output terminal. A capacitor has its ends
connected across the first and second common points. In
another aspect, the filter circuit also includes current
20 limiting protection circuitry for reducing ring trip,
dial pulse and off-hook transient current spikes.

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